PATENT
TUC920010103US1
0018.0108

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:) Examiner: Not Yet Assigned
D.R. Kahler et al.)
Serial No.: 09/998,370)
Filed: November 29, 2001) Art Unit: 2182
For: METHOD, SYSTEM, AND)
PROGRAM FOR ERROR)
HANDLING IN A DUAL)
ADAPTOR SYSTEM)
)

Assistant Commissioner of Patents
Washington, D.C. 20231

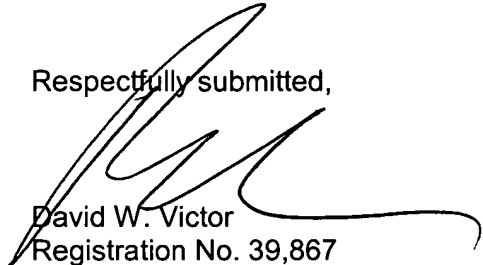## TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

In accordance with 37 CFR §§ 1.56, 1.97, and 1.98, enclosed please find Form PTO-1449 listing the attached references which might be deemed material to the examination of the above-identified application. Applicants submit that no fee is needed nor should be charged with the submittal of this Information Disclosure Statement (IDS) because this IDS is being submitted under 1.97(b) in that the IDS is being filed before the mailing date of a first Office Action on the merits or within three months of the filing date of a national application other than a continued prosecution application under 1.53(d). Applicants submit that a first Office Action has not been received in the above referenced case.

Nonetheless, should any fee be deemed necessary, please charge any insufficiency or credit any overpayment to Deposit Account No. 50-0585. A duplicate of this letter is enclosed for fee processing.

Respectfully submitted,

David W. Victor   Dated: <u>December 12, 2002</u>
Registration No. 39,867
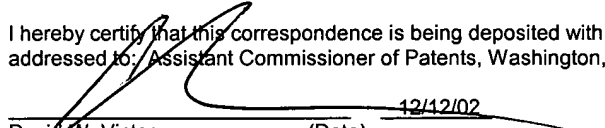KONRAD RAYNES VICTOR & MANN, LLP
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231 on <u>December 12, 2002</u>.

_____   12/12/02
David W. Victor   (Date)

FORM PTO-1449

INFORMATION DISCLOSURE CITATION
IN AN APPLICATION

(Use several sheets if necessary)

Docket Number TUC920010103US1	Application Number 09/998,370
Applicant D.R. Kahler et al.	
Filing Date November 29, 2001	**Group Art Unit** 2182

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER									DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
													RECEIVED	
													DEC 1 8 2002	
													Technology Center 2100	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER									DATE	COUNTRY	CLASS	SUBCLASS	Translation YES NO	

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

		American National Standard for Information Technology. "Serial Storage Architecture- Transport Layer 2 (SSA-TL2)", Revision 5b, April 1, 1997, pp 1-107.
		Blunden, M. and P. Staples. "Understanding SSA Subsystems in Your Environment". © International Business Machines Corporation 2000. 1st Ed., April 2000. pp. 1-151.
		Judd, I.D., P.J. Murfet, and M.J. Palmer. "Serial Storage Architecture", *IBM Journal of Research and Development.* [online] Volume 40, Number 6, 1996. [Retrieved on 12 November 2001]. Retrieved from the Internet at <URL: http://www.research. ibm.com/journal/rd/406/judd.html>
		IBM Corp. "Serial Storage Architecture: A High-Performance Open Storage Interface", [online] © International Business Machines Corporation 1997. [Retrieved on 11 November 2001]. Retrieved from the Internet at <URL: http://ssdweb01. storage.ibm.com/hardsoft/ssaovu.htm>
		U.S. Patent Application Serial No. 09/998,463, filed on November 29, 2001, entitled "Method, System, and Program for Error Handling in a Dual Adaptor System Where One Adaptor is a Master", invented by M.J. Fairhurst, M.J. Jones, V.J. Legvold, and M.P. Vageline.

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.